

FIG. 1

10

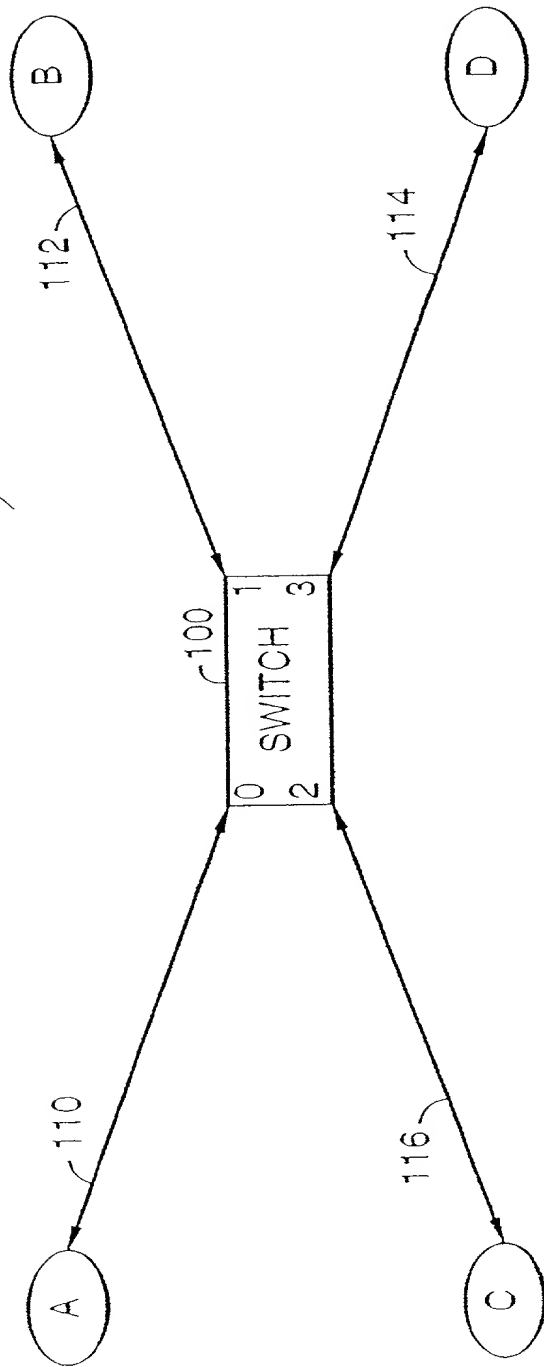


FIG. 3A

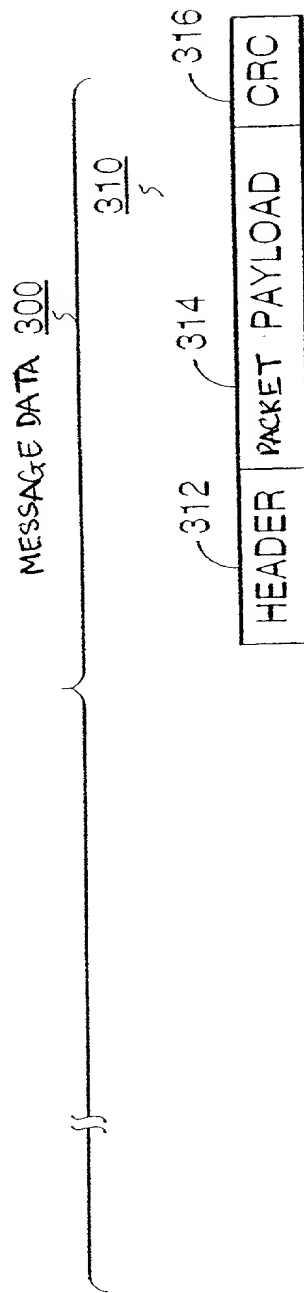
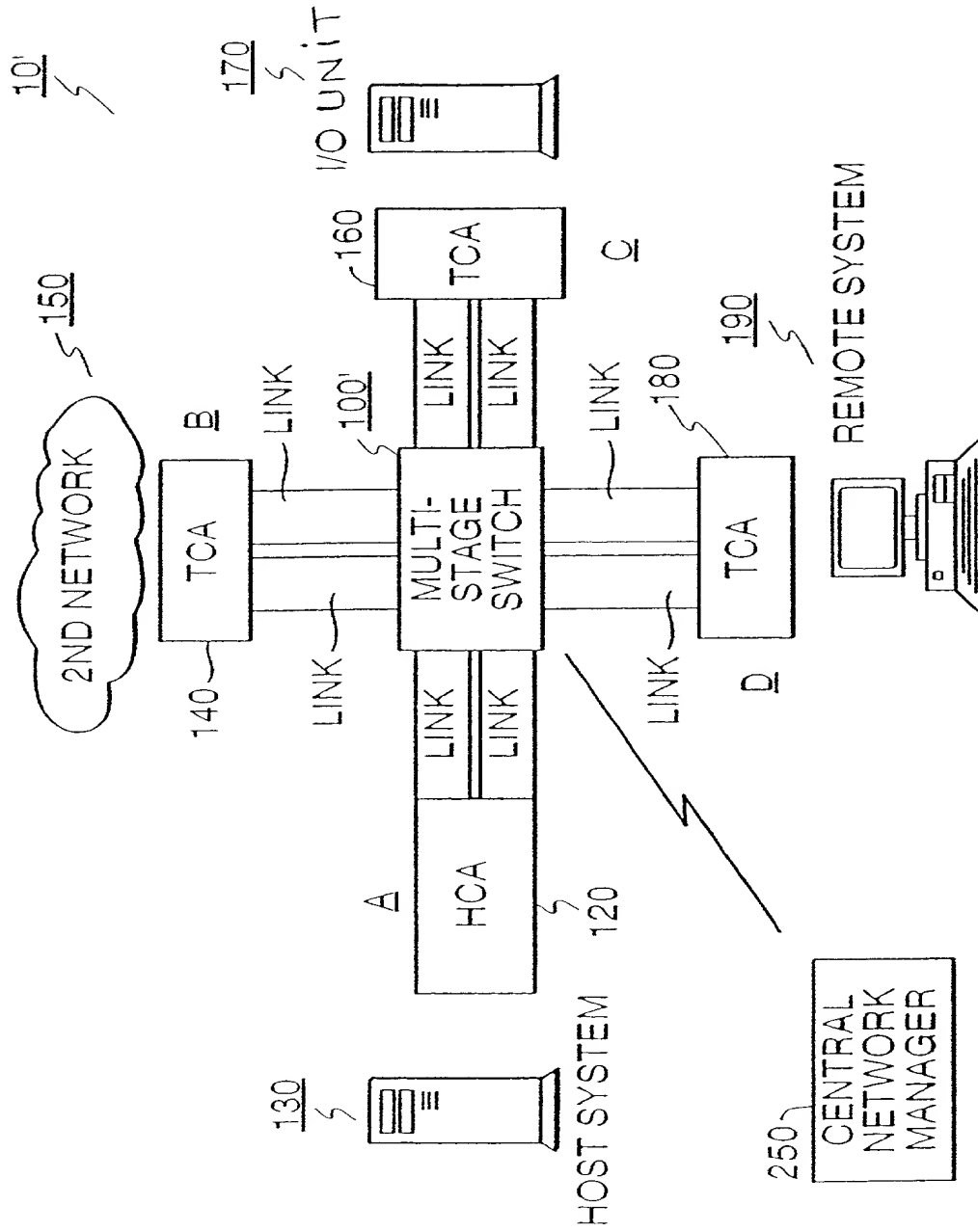
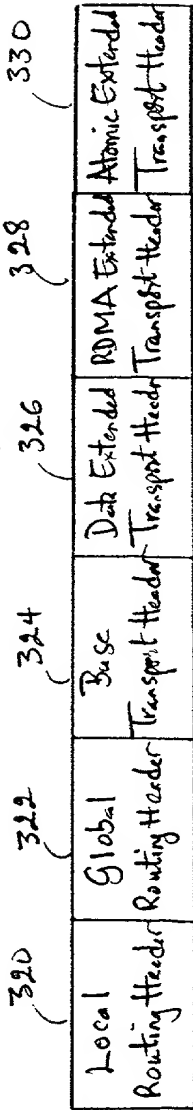


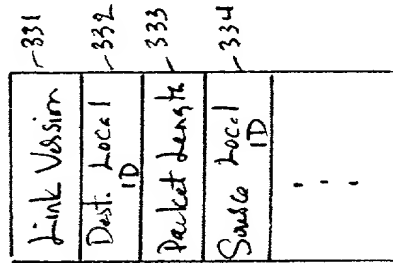
FIG. 2



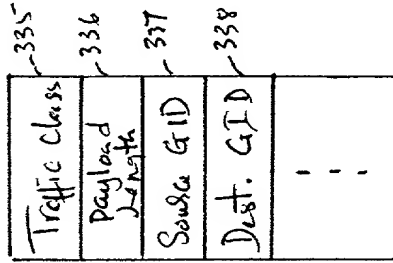
312



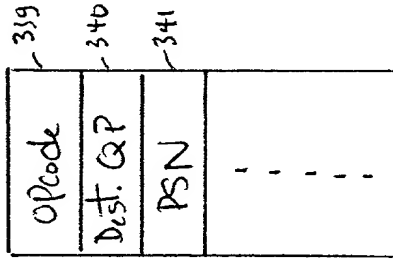
320



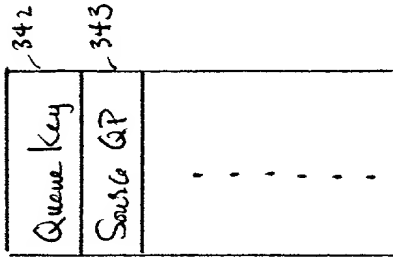
322



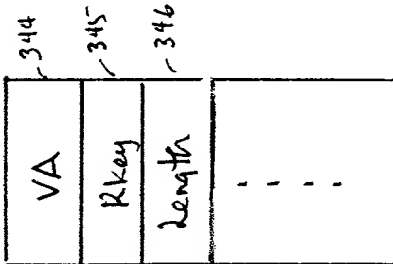
324



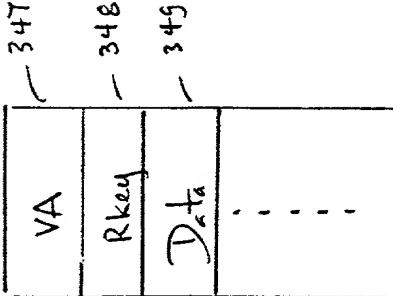
326



328



330



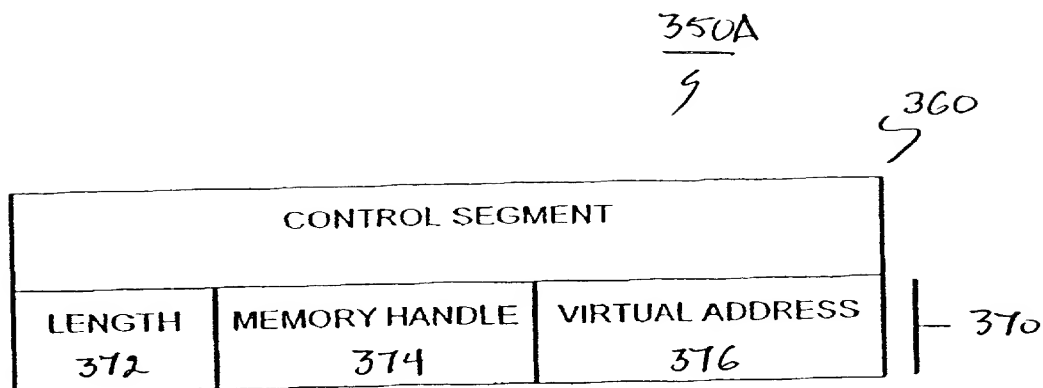


FIG. 3C

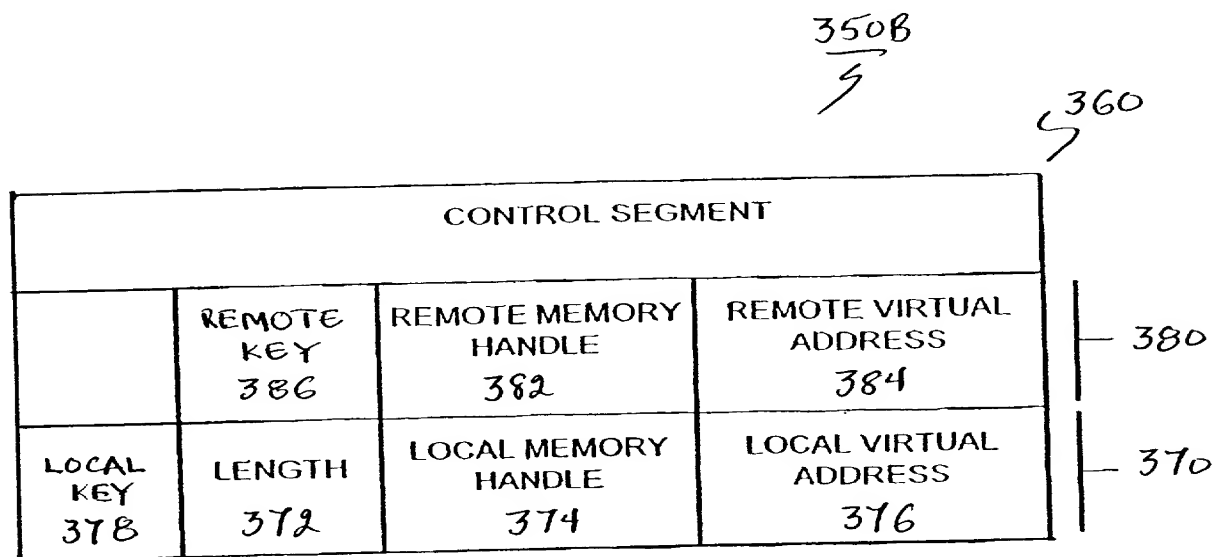
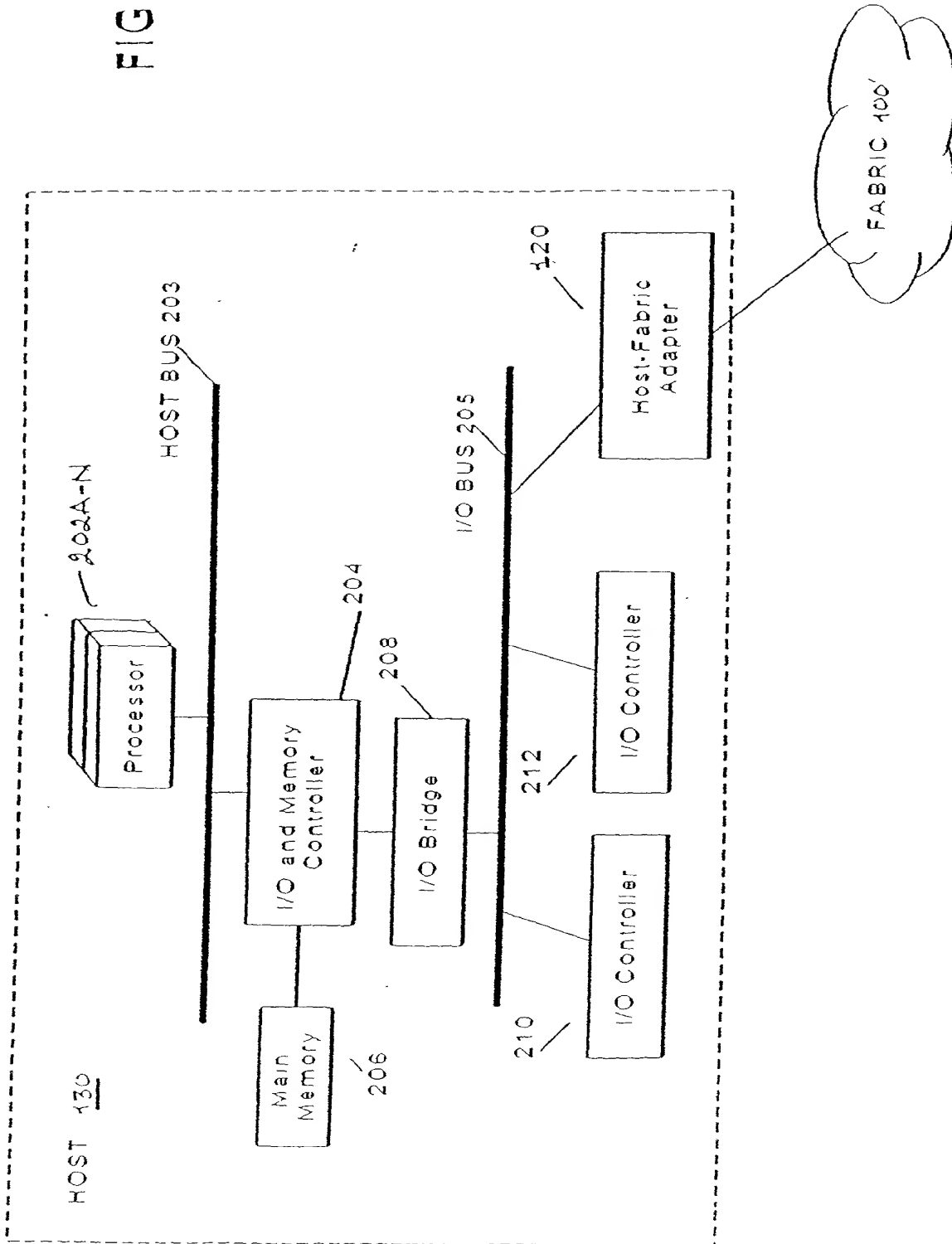
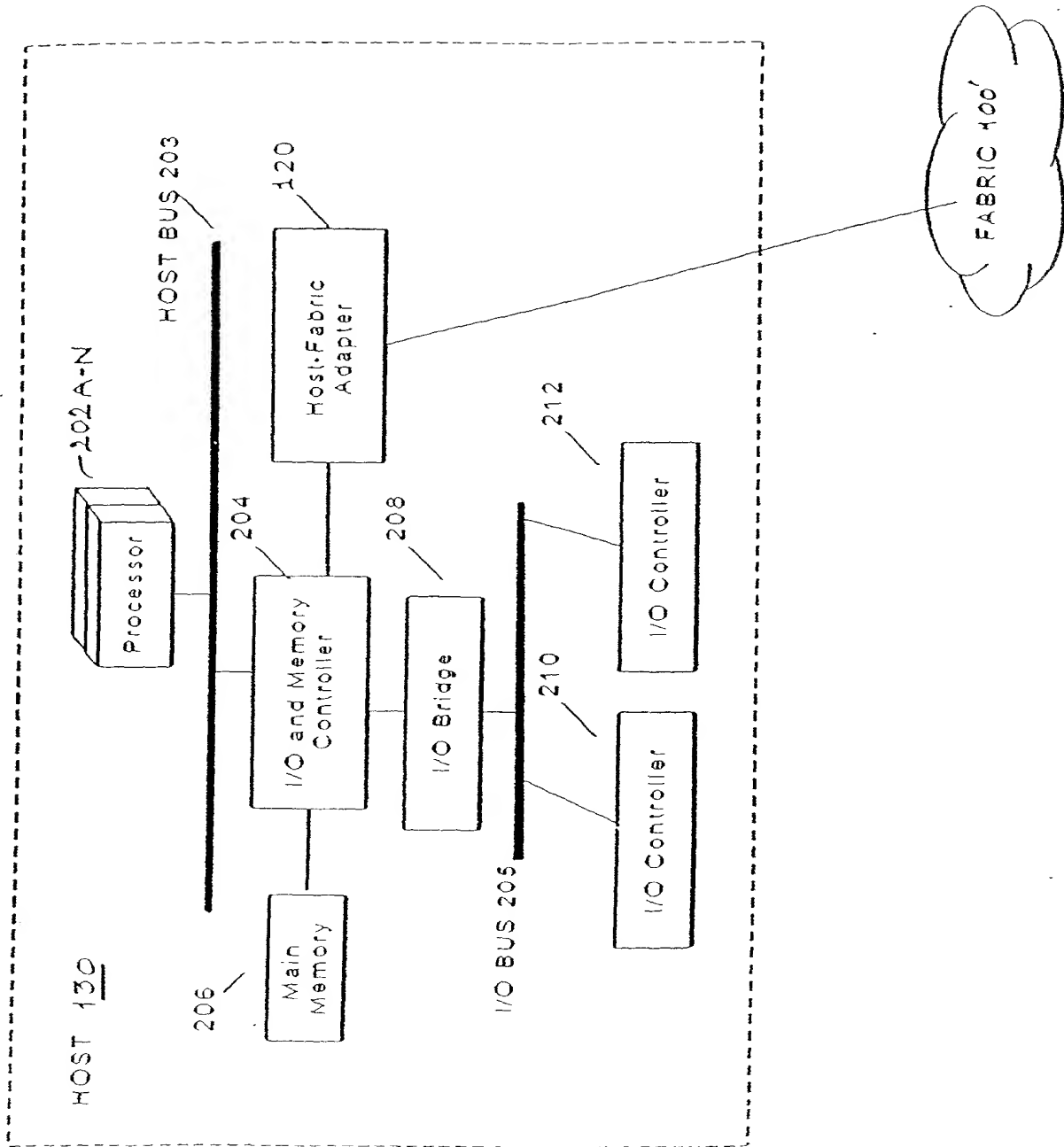
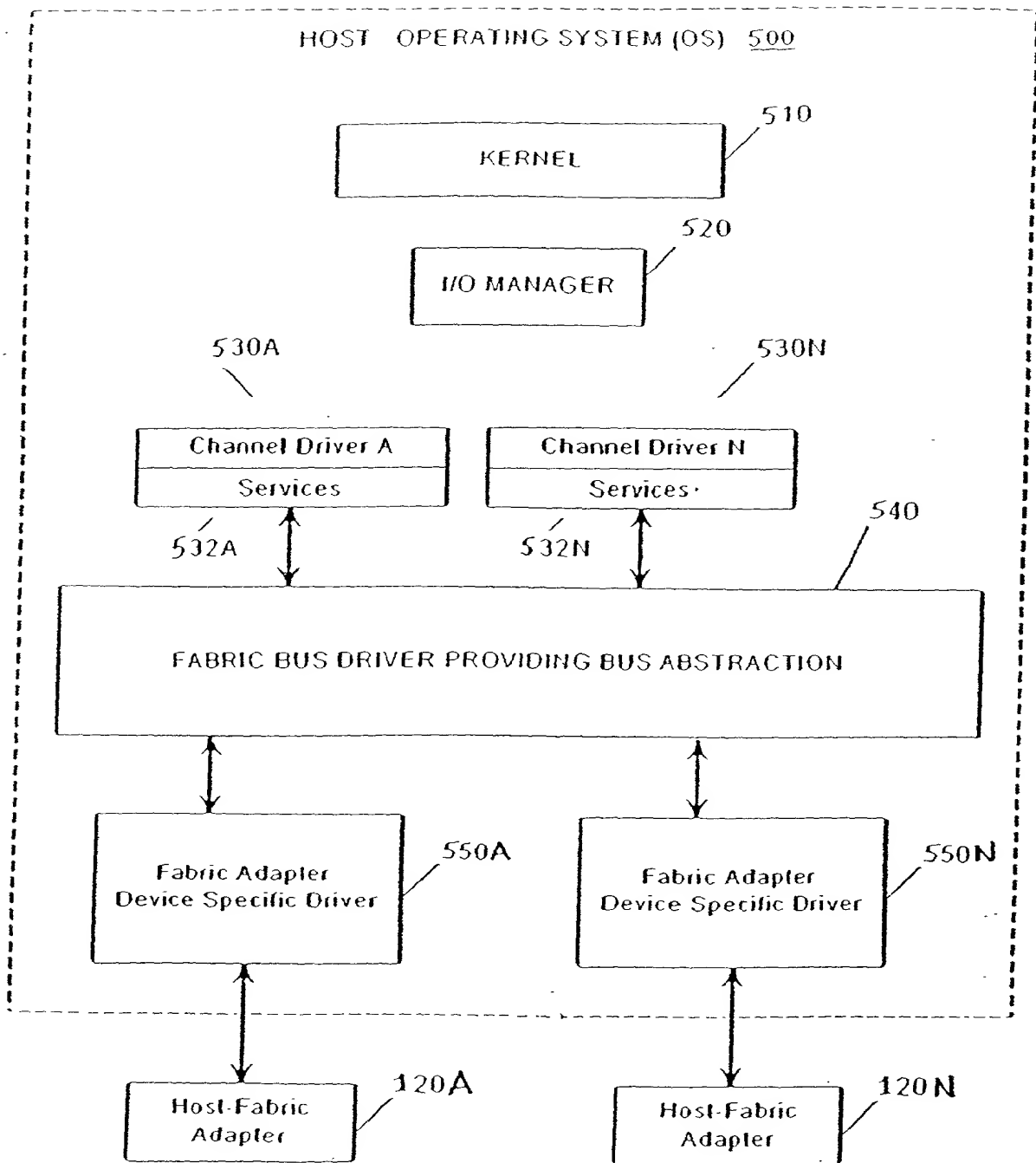


FIG. 3D







EXAMPLE SOFTWARE DRIVER STACKS OF HOST SYSTEM

FIG. 5

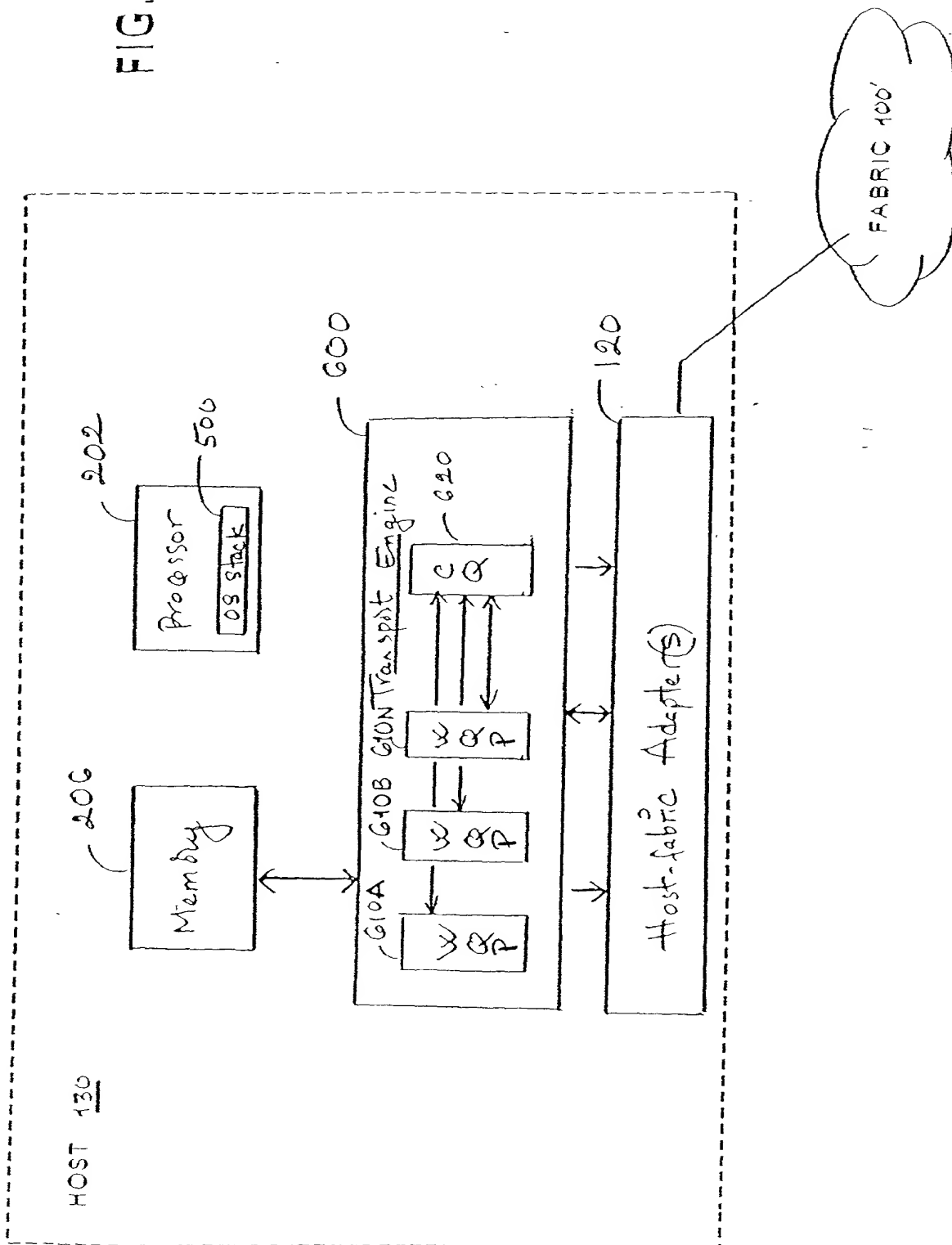




FIG. 7

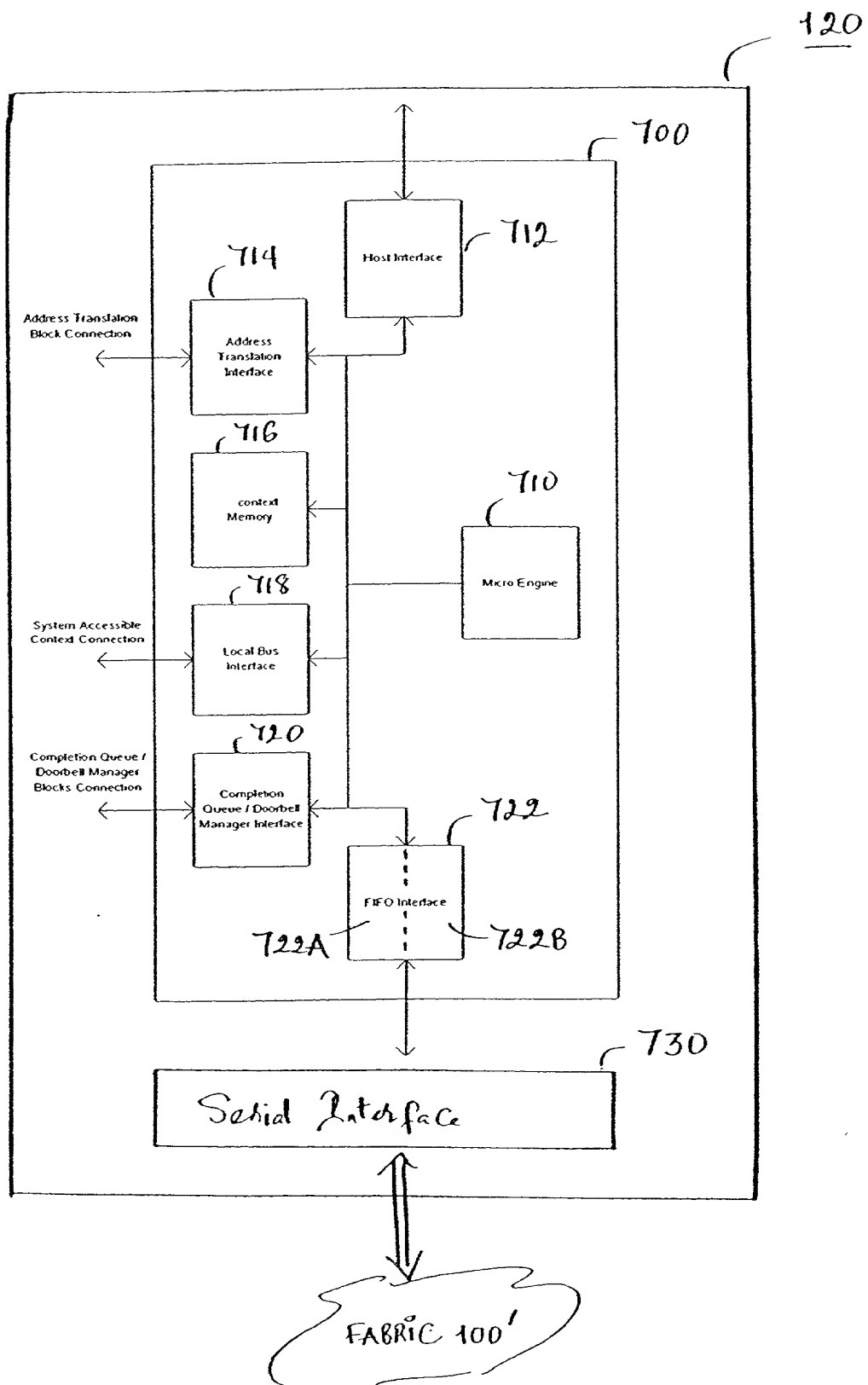


FIG. 8

710  
/

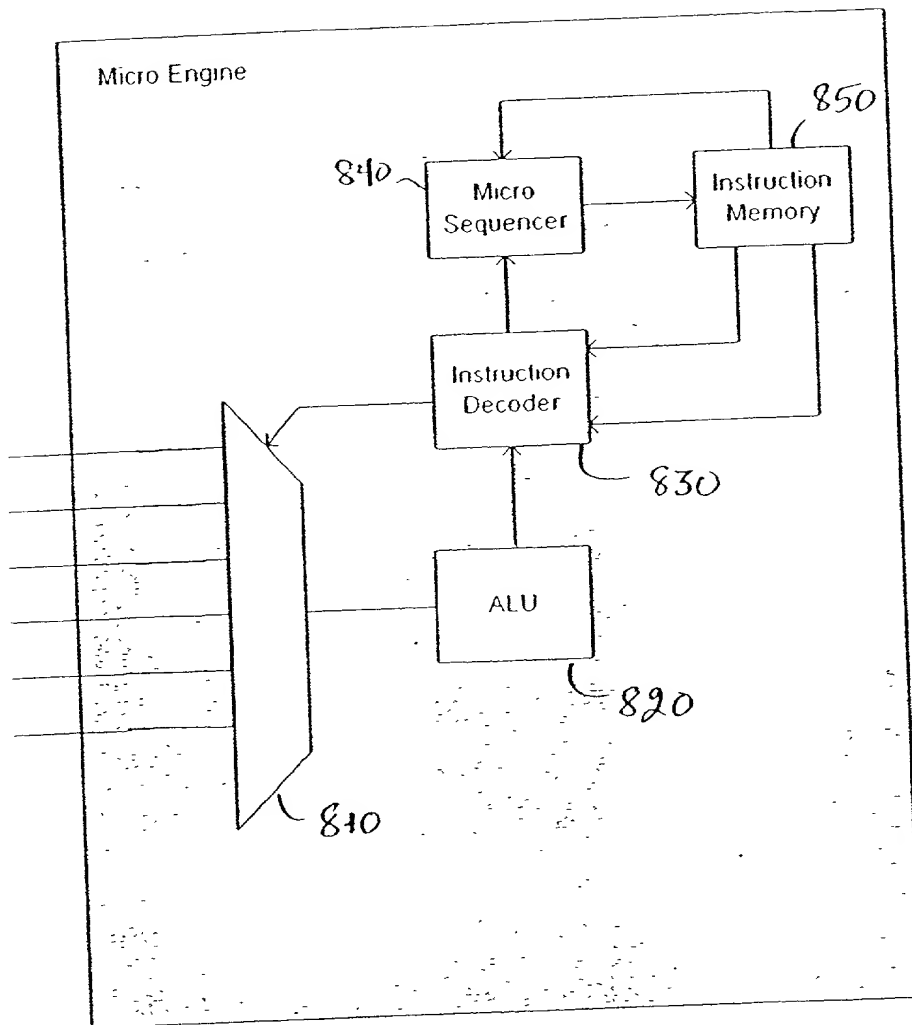
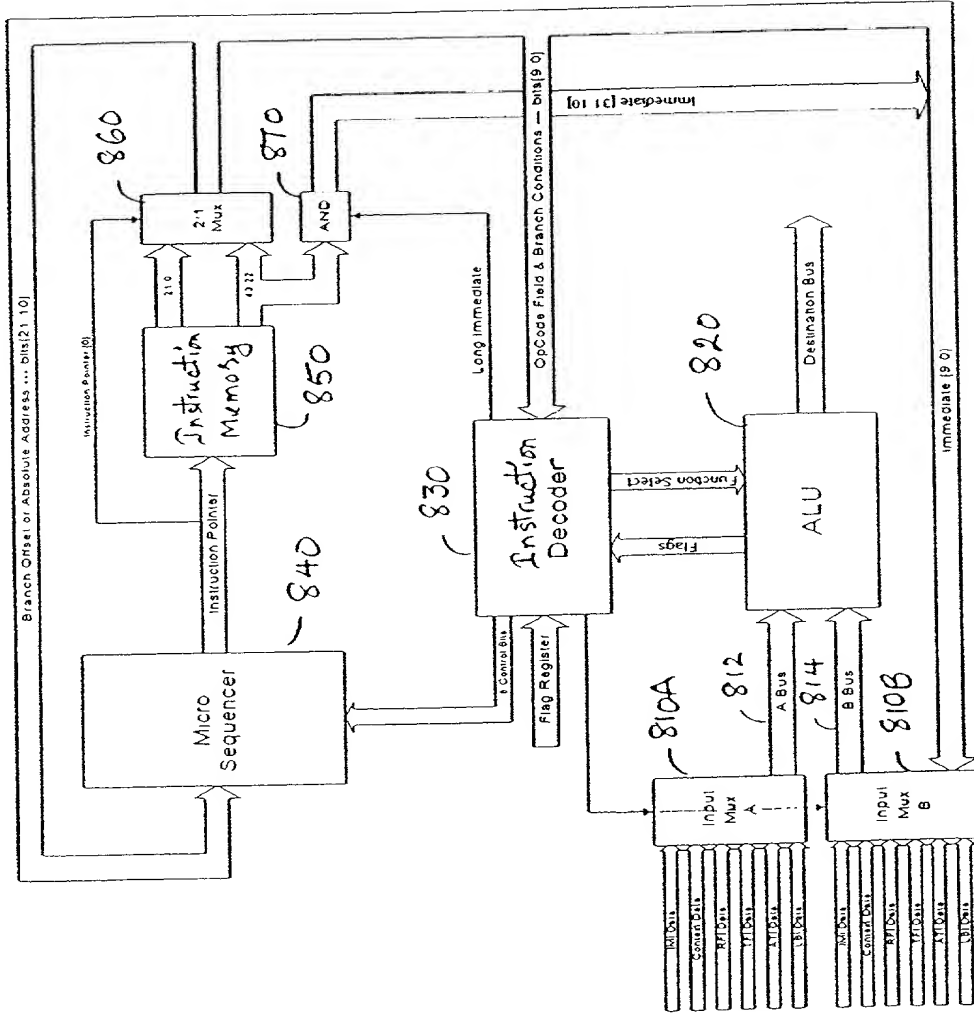


Fig. 9



710

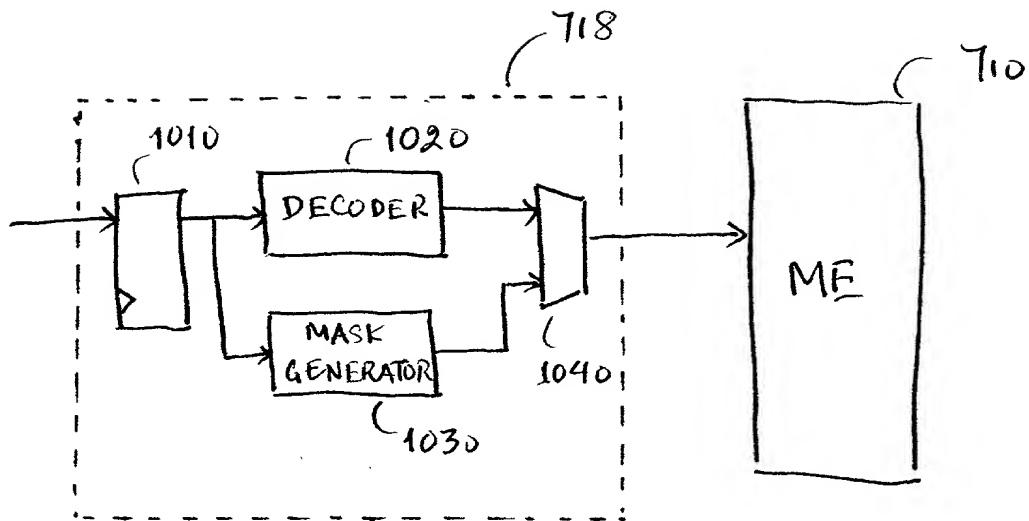


FIG. 10

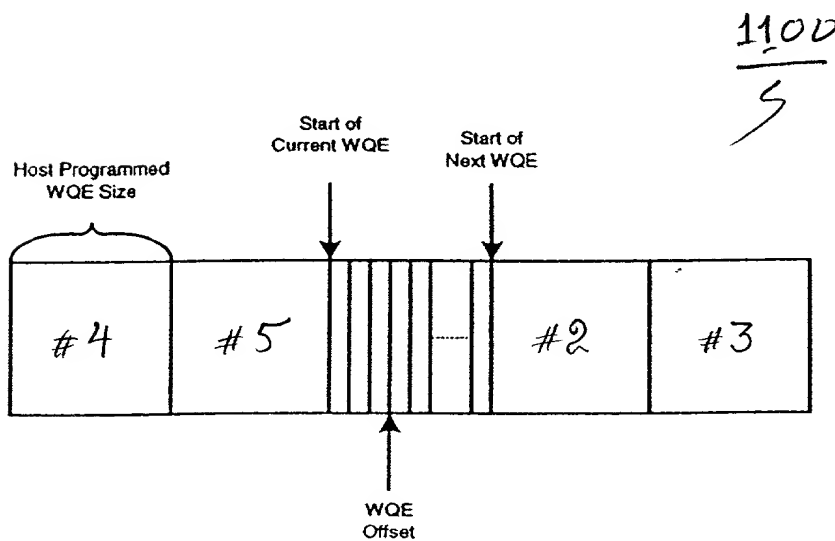
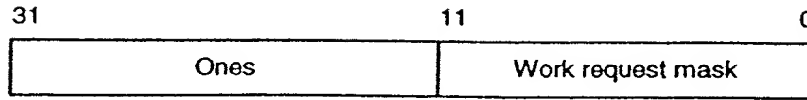


FIG. 11

1200

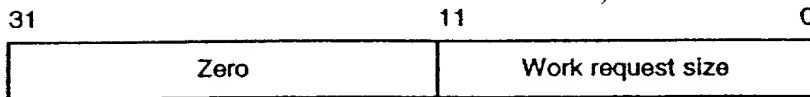


WQE Mask Register

Two bit WQE size (field from host)	Number of Data Segments	Work request mask value
00	1	111111000000
01	5	111110000000
10	13	111100000000
11	29	111000000000

FIG. 12

1300



WQE Size Register

Two bit WQE size (field from host)	Number of Data Segments	Work request size value
00	1	000001000000
01	5	000010000000
10	13	000100000000
11	29	001000000000

FIG. 13

FIG. 14

